

Claims

What is claimed is:

5

1. A signal transmission amplifier circuit, comprising:

a transmission gate having an input coupled to an input signal;

a cross coupled latch coupled to an output of the transmission gate and

10 having a signal output; and

a reference generating circuit coupled to the cross coupled latch.

2. The circuit of claim 1, further including a strobe signal coupled to the transmission gate.

15

3. The circuit of claim 2, wherein the strobe signal is coupled to the cross coupled latch.

4. The circuit of claim 1, wherein the reference generating circuit

20

5. The circuit of claim 4, wherein the reference generating circuit includes a latch coupled to the Schmitt trigger.

25

6. The circuit of claim 5 further including a second transmission gate between the output of the latch and the cross coupled latch.

7. The circuit of claim 1, wherein the input signal is a single ended input.

8. The circuit of claim 1, wherein the input of the transmission gate is coupled to a transmission line.

5

9. A signal transmission amplifier circuit, comprising:

a transmission gate having an input; and

a latch coupled to an output of the transmission gate having a reference

10

input.

10. The circuit of claim 9, wherein the latch is a cross coupled latch.

11. The circuit of claim 9, wherein the transmission gate is coupled to a

15

strobe signal and an inverted strobe signal.

12. The circuit of claim 10, wherein the cross coupled latch latches on an input signal having a voltage that is less than a transistor threshold.

20

13. The circuit of claim 12, wherein the cross coupled latch is coupled to a strobe signal.

14. The circuit of claim 12, further including a reference circuit having a second latch generating the reference input signal.

25

15. A signal transmission amplifier circuit, comprising:

a cross coupled latch having an input; and

a reference voltage generating circuit coupled to the cross coupled latch.

5

16. The circuit of claim 15, wherein the cross coupled latch has a strobe signal input.

17. The circuit of claim 15, further including a transmission gate coupled between the input of the cross coupled latch and an input signal.

10

18. The circuit of claim 15, further including a transmission gate between the reference voltage generating circuit and the cross coupled latch.

19. The circuit of claim 18, wherein the reference generating circuit includes a latch.

15

20. The circuit of claim 19, wherein an input signal is a single ended signal.

20